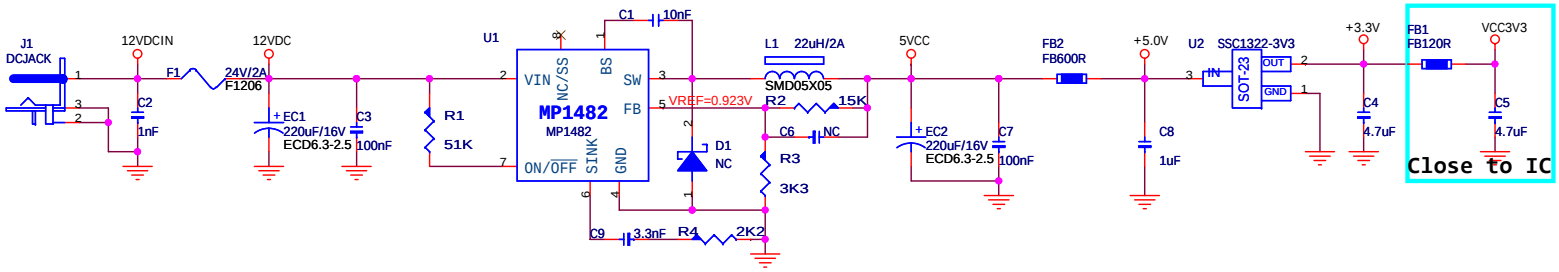
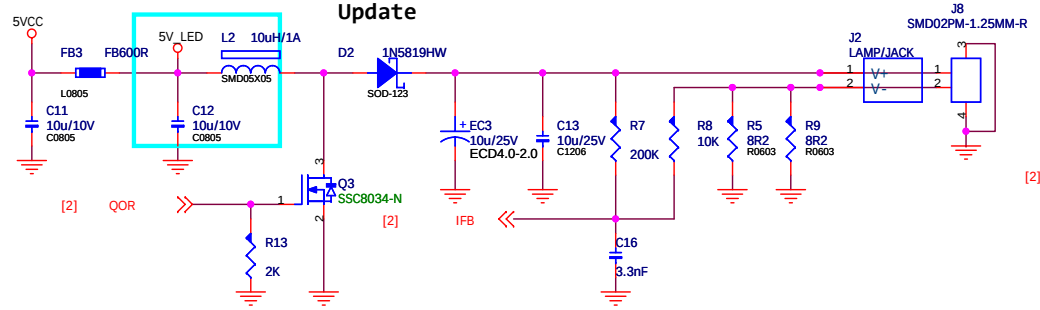
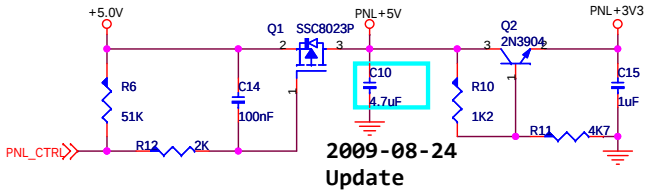


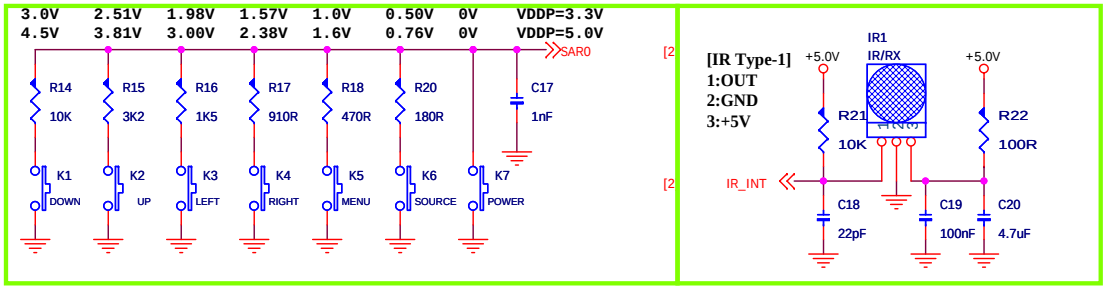
2009-08-24
Update

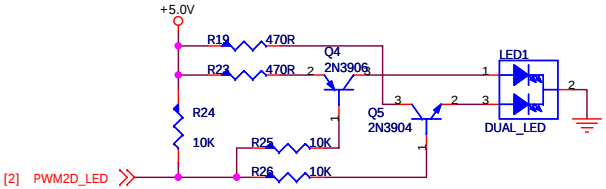


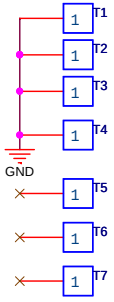


2009-08-24
Update

R5||R9 = [1.2V - (V_led(V) / 21)] / I_led(A)
LED=9.9V/160mA--->R5||R9=4.56R--->4.7R
LED=9.9V/180mA--->R5||R9=4.0R----->3.9R
LED=6.6V/180mA--->R5||R9=4.9R----->5.1R







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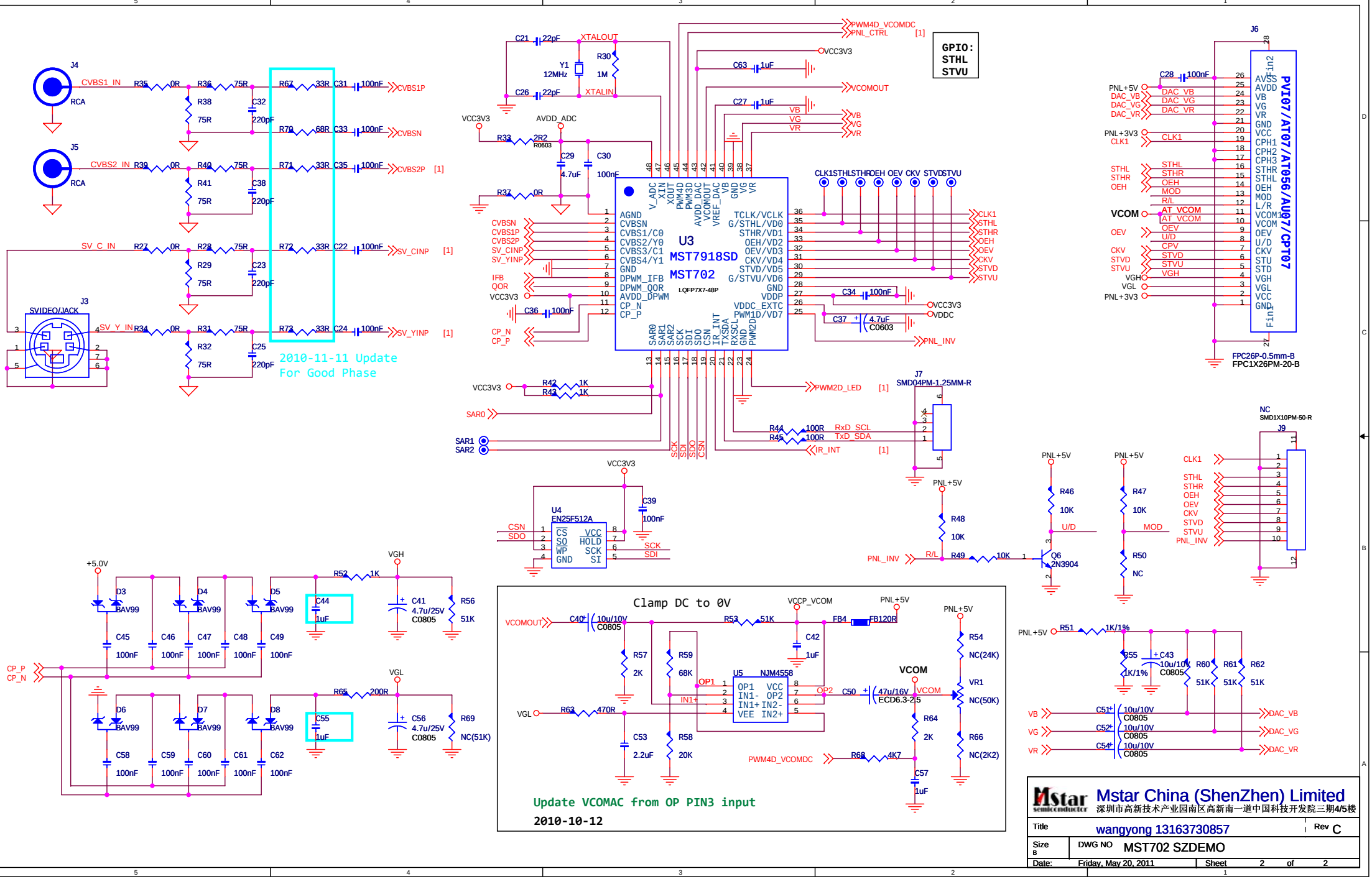
Size B	DWG NO MST702 SZDEMO	
Date: Wednesday, January 05, 2011	Sheet 1 of 2	


GPIO:
STHL
STVU
U3
MST7918SD
MST702
LQFP7X7-48P
U4
EN25F512A
U5
NJM4558
J3
SVIDEO/JACK
J4
J5
J6
J7
SMD04PM-1.25MM-R
J9
NC
SMD1X10PM-50-R
PVI07/AT07/AT056/AU07/CPT07
FPC26P-0.5mm-B
FPC1X26PM-20-B
2010-11-11 Update
For Good Phase
Clamp DC to 0V
Update VCOMAC from OP PIN3 input
2010-10-12
Mstar China (ShenZhen) Limited
深圳市高新技术产业园南区高新南一道中国科技开发院三期4/5楼
Title
wangyong 13163730857
Rev C
Size B
DWG NO MST702 SZDEMO
Date: Friday, May 20, 2011
Sheet 2 of 2

MST702 Revision History

Release	Date	Modification
V1.0 done by Nelson MST702-SZDEMO 100911-V10.DSN	2010.9.11	Preliminary version for review
V1.1 done by Nelson MST702-SZDEMO 101012-V11.DSN	2010.10.12	1.Modify PIN41 to VREF_DAC 2.Modify PIN42 to VCOMOUT 3.Modify PIN43 to AVDD_DAC 4.AV change to CVBS1&2 input, SV change to CVBS3&4 input 5. Modify VCOM OP circuit
V1.1 done by Nelson MST702-SZDEMO 101111-V12.DSN		1.Added R67 R70 R71 R72 R73 For Good Phase
Help		**wangyong** **wang-yong2003@tom.com** **13163730857** **QQ:309464153**

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Title	QQ:309464153	Rev	B
Size B	DWG NO MST702 SZDEMO		
Date: Friday, May 20, 2011	Sheet 1 of 1		